Features

 Supports up to 14.025Gbps bit rates Hot-pluggable SFP+ footprint

40km for SMF transmission

Single +3.3V power supply

◆ 4.25/8.5/14.025G Fibre channel

LC receptacle

RoHS Compliant

Other Optical links

Applications

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• CWDM Cooled EML laser and PIN photodiode, Up to

Compliant with SFP+ MSA and SFF-8472 with duplex

Real Time Digital Diagnostic Monitoring

Operating case temperature: 0 to 70 °C

Datasheet

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SFP+ CWDM 16G 40Km ER

SLSSC-16XX-ER



Overview

SLSSC-16XX-ER SFP+ transceivers high are performance, cost effective modules supporting data rate of 14.025Gbps and 40km transmission distance with SMF.

The transceiver consists of three sections: a Cooled EML laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

The transceivers are compatible with SFP Multi-Source Agreement and SFF-8472 digital diagnostics functions.

Ordering Information

Part Number	Product Description
SLSSC-16XX-ER	SFP+ CWDM 16Gbps, XX = 47 ~ 61, ER 40km, 0ºC ~ +70ºC
Note:	

[1] XX = the wavelength support, available from 1470 (47) ~ 1610 (61) nm

	λc Wavelength Guide										
Code	λc	Unit	Code	λς	Unit	Code	λς	Unit	Code	λς	Unit
47	1470	nm	51	1510	nm	55	1550	nm	59	1590	nm
49	1490	nm	53	1530	nm	57	1570	nm	61	1610	nm

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Module Functional Diagram

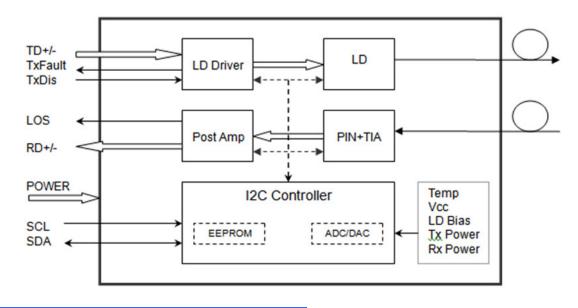


Figure1. Module Functional Diagram

Absolute Maximum rating

Parameters	Symbol	Min.	Max.	Unit
Power Supply Voltage	V _{cc}	-0.5	4.5	V
Storage Temperature	Тс	-40	85	°C
Relative Humidity	RH	5	85	%

Recommended Operating Condition

Parameter	Symbol	Min.	Typical	Max	Unit
Operating Case Temperature	T _c	0	25	70	°C
Power Supply Voltage	V _{CC}	3.135	3.30	3.465	V
Power Supply Current	I _{CC}			550	mA
Data Rate		4.25	14.025		Gb/s

Optical and Electrical Characteristics

Parameter	Symbol	Min	Typical	Max	Unit	Notes
		Transmit	tter			
Centre Wavelength	λc	λc-6.5	λc	λc+6.5	nm	
Spectral Width (-20dB)	Δλ			1	nm	
Side-Mode Suppression Ratio	SMSR	30	-		dB	
Average Output Power	Pout	-1		+3	dBm	1



		ER					
Extinc	Extinction Ratio		8.2			dB	
Data Input Sv	wing Differential	VIN	180		850	mV	2
Input Differe	ntial Impedance	Z _{IN}	90	100	110	Ω	
TX Disable	Disable		2.0		Vcc	V	
TA Disable	Enable		0		0.8	V	
TX Fault	Fault		2.0		Vcc	V	
TX Fault	Normal		0		0.8	V	
	Receiver						
Centre \	Centre Wavelength		1260		1620	nm	
Receive	r Sensitivity				-14	dBm	3
Receive	er Overload		0.5			dBm	3
LOS D	De-Assert	LOS _D			-15	dBm	
LOS	LOS Assert		-28			dBm	
LOS H	LOS Hysteresis		0.5			dB	
Data Output S	Data Output Swing Differential		300		900	mV	4
	LOS	High	2.0		Vcc	V	
	105	Low			0.8	V	

Notes:

1. The optical power is launched into SMF.

2. PECL input, internally AC-coupled and terminated.
3. Measured with a PRBS 2³¹-1 test pattern @14025Mbps, BER ≤1×10⁻¹².

4. Internally AC-coupled.

Timing and Electrical

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			2	ms
Tx Disable Assert Time	t_off			100	μs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	μs
Tx Disable To Reset	t_reset	10			μs
LOS Assert Time	t_loss_on			100	μs
LOS De-assert Time	t_loss_off			100	μs
Serial ID Clock Rate	f_serial_clock		100	400	KHz
MOD_DEF (0:2)-High	V _H	2		Vcc	V
MOD_DEF (0:2)-Low	VL			0.8	V

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Diagnostics

Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal
Voltage	3.0 to 3.6	V	±3%	Internal
Bias Current	0 to 100	mA	±10%	Internal
TX Power	-1 to +3	dBm	±3dB	Internal
RX Power	-16 to -1	dBm	±3dB	Internal

Digital Diagnostic Memory Map

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.

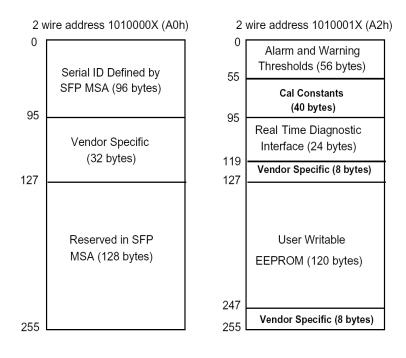


Figure2. Digital Diagnostic Memory Map



Pin Definition

Pin	Signal Name	Description	Plug Seq.	Notes
1	V _{EET}	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	1
3	TX DISABLE	Transmitter Disable	3	2
4	SDA	SDA Serial Data Signal	3	
5	SCL	SCL Serial Clock Signal	3	
6	MOD_ABS	Module Absent. Grounded within the module	3	
7	RSO	Not Connected	3	
8	LOS	Loss of Signal	3	3
9	RS1	Not Connected	3	
10	V _{EER}	Receiver ground	1	
11	V _{EER}	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	4
13	RD+	Received Data Out	3	4
14	V _{EER}	Receiver ground	1	
15	V _{CCR}	Receiver Power Supply	2	
16	V _{CCT}	Transmitter Power Supply	2	
17	V _{EET}	Transmitter Ground	1	
18	TD+	Transmit Data In	3	5
19	TD-	Inv. Transmit Data In	3	5
20	V _{EET}	Transmitter Ground	1	

Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

- TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2. Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
- 3. LOS is open collector output. Should be pulled up with 4.7k~10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.
- 4. RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
- 5. TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.



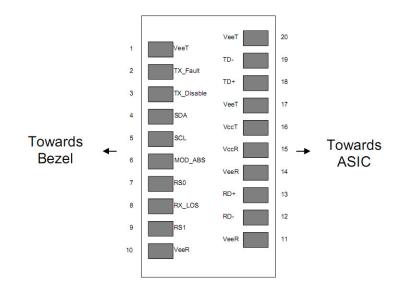


Figure3. Electrical Pin-out Details

Recommended Interface Circuit

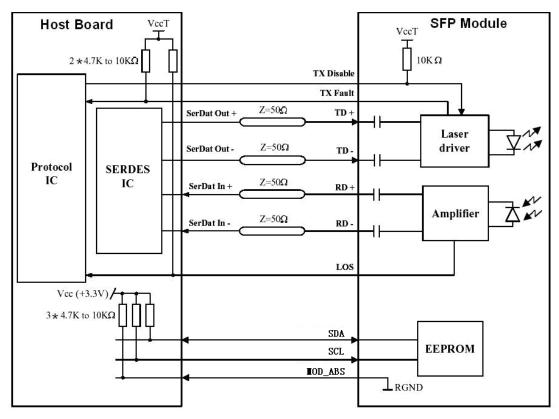
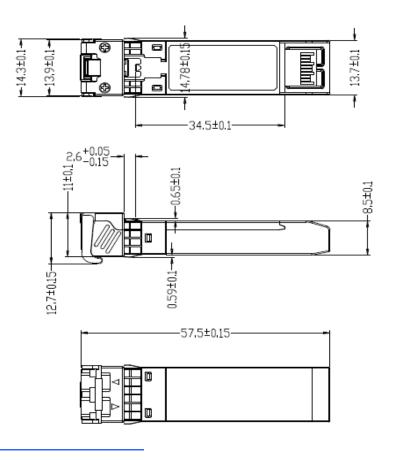


Figure4. Recommended Interface Circuit

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Mechanical Specifications





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